**EEM16- HW#4**

**Ch. 7: 3, 5, 9, 15, 19**
Note: for 7.19, just do a minimum number of states approach. You don’t need to use the minimization procedure – use your knowledge to design the state machine with the least number of states possible.

**Ch. 8: 5, 6, 17, 27**
Note: for 8.17, make sure you produce next-state equations (derived from FF inputs), encoded transition & output table, coded-to-symbolic state mapping, symbolic transition & output table, state diagram and a high-level description. Then, use the state diagram to come up with a timing diagram (time behavior) for an arbitrary input sequence of your choice (~10-15 inputs should be sufficient).