Analog Integrated Circuit Design

Instructor: Hooman Darabi  
(949) 926-5817, hdarabi@broadcom.com  
Office Hours: Tuesday 9:00-10:00 AM, 66144-EIV & With Appointment

Time: TR 10:00-11:50 AM

Place: Boelter 2760

Prerequisite: EE115B from UCLA or equivalent

Credit: 4 Units

Course web page: http://www.eeweb.ee.ucla.edu/classinfo.php?/ee215A/1/fall/4  
Check the web page regularly for latest news!

Grading:  
Homeworks 10%  
Midterm 30%, Nov. 4, 10:00-11:50 AM  
Final 40%, Dec. 16, 3:00-6:00 PM  
Design Project 20%, Dec. 7, 10:00 AM

Recommended Reading:  
Analysis and Design of Analog Integrated Circuits, by Gray, Hurst, Lewis, and Meyer  
Design of Analog CMOS Integrated Circuits, by Razavi  
IEEE Journal of Solid-State Circuits  
ISSCC Digest of Technical Papers