1) The topics from Lecture, Review Sessions, and Homework will be included. Primary topics to be covered on our Final Exam

i) BJT Bias Circuits
ii) BJT Amplifiers
iii) MOSFET and CMOS Bias Circuits
iv) MOSFET and CMOS Amplifiers

v) Digital CMOS Applications will not be included
vi) CMOS Differential Amplifiers and Operational Amplifiers not be included

vii) Important general topics concern one and two transistor circuits and include computation of

   (1) Gain
   (2) Input Resistance
   (3) Output Resistance

2) Additional Office Hours will be held this coming week

   a) Tuesday  6/15   3:00 – 6:00
   b) Wednesday  6/16   3:00 – 5:00
   c) Thursday  6/17   3:00 – 5:00

3) For the Exam please bring:

   a) Blue or Yellow Exam Books
   b) One 8-1/2” x 11” Notes Sheet both sides with notes
   c) Spare calculator batteries